



DOCKET NO.: 241341US2S/sal

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: Keiji HOSOTANI

SERIAL NUMBER: 10/635,605

GROUP: 2826

FILED: August 7, 2003

EXAMINER: PERT, EVAN T

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING  
MAGNETORESISTIVE EFFECT DEVICE AND METHOD OF  
MANUFACTURING THE SAME

**REQUEST TO CORRECT TITLE OF INVENTION**

MAIL STOP ISSUE FEE  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

SIR:

In the matter of the above-identified application for patent, we hereby request  
correction of your records to reflect the correct title of the invention. The title of the  
invention should read as follows: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
INCLUDING MAGNETORESISTIVE EFFECT DEVICE AND METHOD OF  
MANUFACTURING THE SAME.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

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(OSMMN 05/04)



Date Allowed: December 21, 2005  
Docket No.: 241341US2S



COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/635,605  
Applicants: Keiji HOSOTANI  
Filing Date: August 7, 2003  
For: SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE AND METHOD OF MANUFACTURING  
THE SAME  
Group Art Unit: 2826  
Examiner: PERT, EVAN T.

SIR:

Attached hereto for filing are the following papers:

**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters

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Docket No.: 241341US2S

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: Keiji HOSOTANI DATE ALLOWED: Dec. 21, 2005

SERIAL NUMBER: 10/635,605

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FILED: August 7, 2003

EXAMINER: PERT, EVAN T.

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD OF  
MANUFACTURING THE SAME

**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

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SIR:

The Examiner's Statement of Reasons for Allowance dated July 13, 2005, recites the Reasons for Allowance for Claims 13-21 as including a "metal" layer on the insulating layer. Claims 13, 16, and 19 recite a "conductive" layer, rather than a "metal" layer. Because not all conductive elements are necessarily metallic, Applicant respectfully requests a revised Reasons for Allowance be issued which recites the respective portions of all allowed Claims 13, 16, and 19, which recite "conductive" rather than "metal."

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

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